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**To: Examiner: Parviz Hassanzadeh  
Group Art Unit: 1763  
Confirmation No.: 1482**

**From: John S. Sensny, Esq.**

**MAR 17 2005**

**Fax: 703-872-9306**

**Pages: 7 (Including fax cover sheet)**

**Phone:**

**Date: March 17, 2005**

**Re: Applicants: Casimer M. DeCusatis, et al.  
Serial No.: 09/976,545  
Filed: October 12, 2001  
For: "REAL-TIME PROCESS CONTROL  
FOR OPTICAL COMPONENT FABRICATION  
USING WAVELENGTH LOCKED LOOPS"  
Docket: FIS920010130US1 (14563)  
JSS:jy**

**CC**

**RESPONSE TO EXAMINER'S STATEMENT OF  
REASONS FOR ALLOWANCE**

**TRANSMITTED HERewith:**

1. General Transmittal Letter (in duplicate);
2. Response to Examiner's Statement of Reasons for Allowance;
3. Certificate of Transmission of Facsimile, dated March 17, 2005.


**FOR:**

**Applicants: Casimer M. DeCusatis, et al.  
Serial No.: 09/976,545  
Filed: October 12, 2001  
For: "REAL-TIME PROCES CONTROL FOR OPTICAL COMPONENT FABRICATION  
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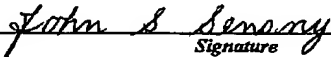
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<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			<b>Docket No.</b>
Applicant(s): <b>Casimer M. DeCusatis, et al.</b>			<b>FIS920010130US1 (14563)</b>
<b>Application No.</b> 09/976,545	<b>Filing Date</b> October 12, 2001	<b>Examiner</b> Parviz Hassanzadeh	<b>Group Art Unit</b> 1763
Invention: <b>REAL-TIME PROCESS CONTROL FOR OPTICAL COMPONENT FABRICATION USING WAVELENGTH LOCKED LOOPS</b>			
<p>I hereby certify that this <u><b>RESPONSE TO EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE</b></u> (Identify type of correspondence) is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>(703) 872-9306</u> ) on <u><b>March 17, 2005</b></u> (Date)</p> <p style="text-align: right;"><u><b>John S. Sensny</b></u> (Typed or Printed Name of Person Signing Certificate) <u></u> (Signature)</p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p>			

P18/REV02

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<b>TRANSMITTAL LETTER</b> (General - Patent Pending)				Docket No. <b>FIS920010130US1 (14563)</b>	
In Re Application Of: <b>Casimer M. DeCusatis, et al.</b>					
Application No. <b>09/976,545</b>	Filing Date <b>October 12, 2001</b>	Examiner <b>Parviz Hassanzadeh</b>	Customer No. <b>23389</b>	Group Art Unit <b>1763</b>	Confirmation No. <b>1482</b>
Title: <b>REAL-TIME PROCESS CONTROL FOR OPTICAL COMPONENT FABRICATION USING WAVELENGTH</b>					
<u>COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is: <b>RESPONSE TO EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE</b>					
in the above identified application.					
<input checked="" type="checkbox"/> No additional fee is required.					
<input type="checkbox"/> A check in the amount of _____ is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <b>09-0458/IBM</b> as described below.					
<input type="checkbox"/> Charge the amount of _____					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional fee required.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<b>WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.</b>					
<div style="text-align: center;"> Signature</div> <b>John S. Sensny</b> Registration No. 28,757  Scully, Scott, Murphy & Presser 400 Garden City Plaza Garden City, New York 11530 (516) 742-4343			Dated: <b>March 17, 2005</b>		
cc: JSS:jy			<div style="border: 1px solid black; padding: 2px;">I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1480, Alexandria, VA 22313-1450" [37 CFR 4.8(a)] on _____</div> <div style="border: 1px solid black; padding: 2px;">(Date)</div> <div style="border: 1px solid black; padding: 2px;">Signature of Person Mailing Correspondence</div> <div style="border: 1px solid black; padding: 2px;">Typed or Printed Name of Person Mailing Correspondence</div>		

P16A/REV03

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****RECEIVED  
CENTRAL FAX CENTER****MAR 17 2005****Applicants:** Casimer M. DeCusatis, et al.**Examiner:** Parviz Hassanzadeh**Serial No.:** 09/976,545**Group Art Unit:** 1763**Filed:** October 12, 2001**Docket:** FIS920010130US1 (14563)**For:** REAL-TIME PROCESS CONTROL  
FOR OPTICAL COMPONENT FABRICATION  
USING WAVELENGTH LOCKED LOOPS**Dated:** March 17, 2005**Confirmation No.:** 1482Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**RESPONSE TO EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE**

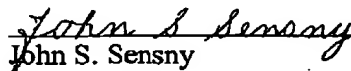
Sir:

In an Examiner's statement of reasons for allowance, which was attached to the Notice of Allowance, the Examiner read the limitation "an optical signal generated by a dither source" into independent Claim 11.

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**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

**Dated:** March 17, 2005  
John S. Sensny**BEST AVAILABLE COPY**

It is important to emphasize that, as described in Claim 11, the optical signal is not generated by the dither source. Instead, the optical signal is generated by "a light subsystem."

Applicants' Attorneys have carefully reviewed Claim 11 and the prior art, and the Examiner's apparent misquoting of Claim 11 does not affect the allowability of the claim.

If the Examiner has any questions about this matter, the Examiner is asked to telephone the undersigned.

Respectfully submitted,

*John S. Sensny*  
John S. Sensny  
Registration No. 28,757  
Attorney for Applicants

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